

AMENDED CLAIMS

1. (currently amended) A liquid crystal display for testing defects of wiring in a panel, comprising: a TFT array unit ~~comprising~~ including a plurality of gate lines and data lines formed in a matrix shape, having TFT transistors at the intersection of each of the gate line lines and ~~the data line lines~~; a data pad unit commonly connected to the plurality of data lines; for receiving signals for driving the data lines; and a wiring unit for testing defects of the data line; being connected between the data pad unit and the data line, for testing disconnection and short anomalies of the data line.

2. (currently amended) The liquid crystal display according to claim 1, wherein the data pad unit ~~comprising~~ further comprises a first data pad ~~unit~~ commonly connected ~~between to the~~ odd data lines of the plurality of data lines, for receiving signals for driving the odd data lines and a second data pad ~~unit~~ commonly connected ~~between to the~~ even data lines of the plurality of data lines; for receiving signals for driving the even data line.

3. (currently amended) The liquid crystal display according to claim 2, wherein the wiring unit for testing defects of the data line further comprises a first wiring unit for testing defects of the data line connected between the first data pad ~~unit~~ and the odd data line; for testing disconnection and short anomalies of the odd data line and a second wiring unit for testing defects of the data line connected between the second data ~~pad-unit pad~~ and the even data line, for testing disconnection and short anomalies of the even data line.

4. (currently amended) The liquid crystal display according to claim 3, wherein the first and the second wiring units for testing defects of the data line are formed in a zigzag shape.

5. (currently amended) The liquid crystal display for testing defects of wiring in a panel comprising: a TFT array unit ~~comprising~~ including a plurality of gate lines and data lines formed in a matrix shape, having TFT transistors at the intersection of ~~the~~ each gate line and ~~the data line~~; a gate pad unit commonly connected between the plurality of gate lines; for receiving signals for

driving the gate line; and a wiring unit for testing defects of the gate line connected being between the gate pad unit and the gate line; for testing disconnection and short anomalies of the gate line.

6. (currently amended) The liquid crystal display according to claim 5, wherein the gate pad unit comprises a first gate pad ~~unit~~ commonly connected ~~between~~ to the odd gate lines of the plurality of gate lines; for receiving signals for driving the odd gate lines and a second gate pad ~~unit~~ commonly connected ~~between~~ to the even gate lines of the plurality of gate lines; for receiving signals for driving the even gate lines.

7. (currently amended) The liquid crystal display according to claim 6, wherein the wiring unit for testing defects of the gate line further comprises a first wiring unit for testing defects of the gate line connected between the first gate pad ~~unit~~ and the odd gate line; for testing disconnection and short anomalies of the odd gate line and a second wiring unit for testing defects of the ~~second~~ gate line connected between the second gate pad ~~unit~~ and the even gate line; for testing disconnection and short anomalies of the even gate line.

8. (currently amended) The liquid crystal display according to claim 7, wherein the first and the second wiring units for testing defects of the gate line are formed in a zigzag shape.

9. (currently amended) A liquid crystal display for testing defects of wiring in a panel comprising: a TFT array unit ~~comprising~~ including a plurality of gate lines and data lines formed in a matrix shape, having a TFT transistors in each pixel at the intersection of ~~the~~ each gate line and ~~the~~ data line; a common voltage pad unit for applying a common voltage to a common voltage line connected to each pixel; and a wiring unit for testing defects of the common voltage line connected between the common voltage line and the common voltage pad unit; for testing disconnection and short anomalies of the common voltage line.

10. (currently amended) The liquid crystal display according to claim 9, wherein the common voltage pad unit comprises a first common voltage pad ~~unit~~ commonly connected ~~between~~ to the odd common voltage line of the plurality of common voltage lines; for receiving signals for driving the odd common voltage line and a second common voltage pad ~~unit~~ commonly connected ~~between~~ to the even common voltage lines of the plurality of common voltage lines; for receiving signals for driving the even common voltage line.

11. (currently amended) The liquid crystal display according to claim 10, wherein the wiring unit for testing defects of the common voltage line further comprises a first wiring unit for testing defects of the common voltage line connected between the first common voltage pad ~~unit~~ and the odd common voltage line; for testing disconnection and short anomalies of the odd common voltage line and a second wiring unit for testing defects of the common voltage line connected between the second common voltage pad ~~unit~~ and the even common voltage line; for testing disconnection and short anomalies of the even common voltage line.

12. (currently amended) The liquid crystal display according to claim 11, wherein the first and the second wiring units for testing defects of the common voltage line preferably are formed in a zigzag shape.